



<b>Form PTO-1449 U.S. Department of Commerce (REV. 2-82) Patent and Trademark Office</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	Atty. Docket No. 070050.1280 (AP32158)	Serial No. 09/591,270
	Applicant K. Shepard	
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**U.S. PATENT DOCUMENTS**

Exam. Init.	Document No.	Date	Name	Class	Subclass	Filing Date if Appro.

**FOREIGN PATENT DOCUMENTS**

Document No.	Date	Country	Class	Subclass	Translation Yes No

**OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)**

<i>Appl</i>		Lu et al., "Floating-body Effects in Partially SOI CMOS circuits", 32 IEEE J. Solid-Stated Circuits 1241 (1997).
<i>Appl</i>		D.H. Allen et al., "A 0.20 $\mu$ m 1.8 V SOI 550 MhZ 64b Power PC Microprocessor with Cu Interconnects", Digest Tech. Papers, ISSCC, pp. 438-39, (1999).
<i>Appl</i>		C.T. Chuang et al., "SOI Digital CMOS VLSI – A Design Perspective", 36th ACM/IEEE Design Automation Concerence, pp.709-714, (1999).
<i>Appl</i>		J. Gautier et al., "On the Transient Operation of Partially Depleted SOI NMOSFET's", 16 IEEE Electron Device Letters 498 (1995).
<i>Appl</i>		R. Puri et al., "Hysteresis Effect in Pass-Transistor-Based Partially-Depleted SOI CMOS Circuits", Proc. Int'l SOI Conf., 1998.

Examiner <i>Appl J. Sherron</i>	Date Considered <i>7/28/03</i>
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\* Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.